

MEMORY CONFIGURATION APPARATUS, SYSTEMS, AND METHODS

Abstract of the Disclosure

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An apparatus and a system, as well as a method and article, may operate to control a bandwidth of a memory coupled to a plurality of data processing units responsive to protocol indications, such as a number of data processing units in use. In some embodiments, apparatus and systems, as well as methods and articles, may
10 operate to select a memory access group size of about 2^N memory banks responsive to receiving an indication of a change in a protocol type, wherein the group is selected from a number B of banks, and N is associated with the protocol type.

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